

# C5750CH2J104J280KC

TDK Item Description : C5750CH2J104JT\*\*\*\*



## Application & Main Feature

Commercial Grade ( Mid Voltage (100 to 630V) )

## Series

C5750 [EIA 2220]



## Dimensions

L	5.70mm ±0.40mm
W	5.00mm ±0.40mm
T	2.80mm ±0.30mm
B	0.20mm Min.
G	

## Temperature Characteristic

CH (-25 to 85 degC 0+/-60ppm/degC)

## Rated Voltage

2J(630Vdc)

## Capacitance

100nF

## Capacitance Tolerance

J (±5%)

## Q Factor

1000 Min.

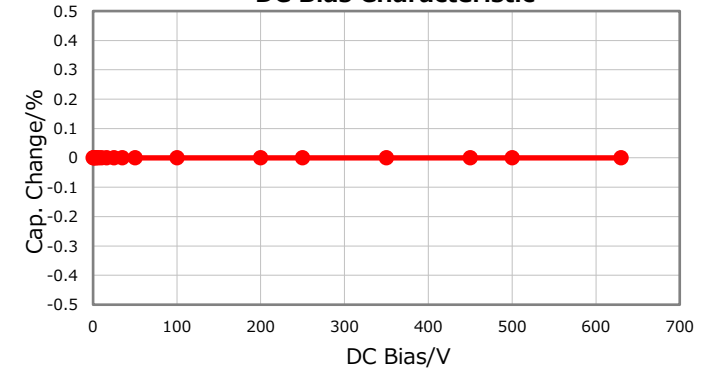
## Insulation Resistance

5Gohm Min

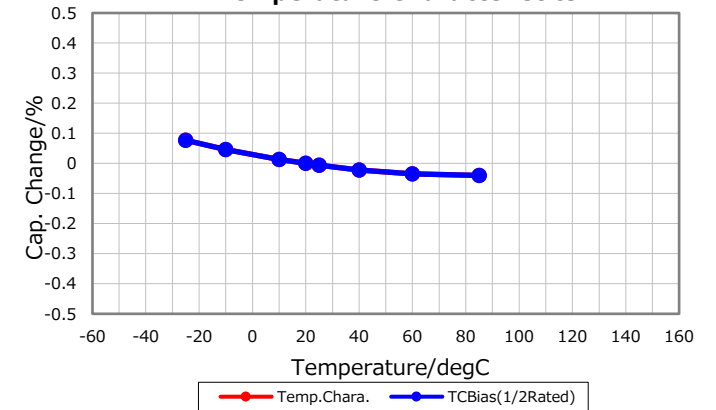
## AEC-Q200

Not Applicable

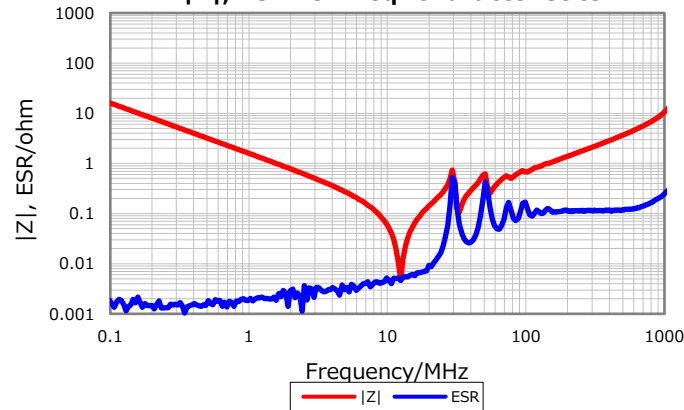
## DC Bias Characteristic



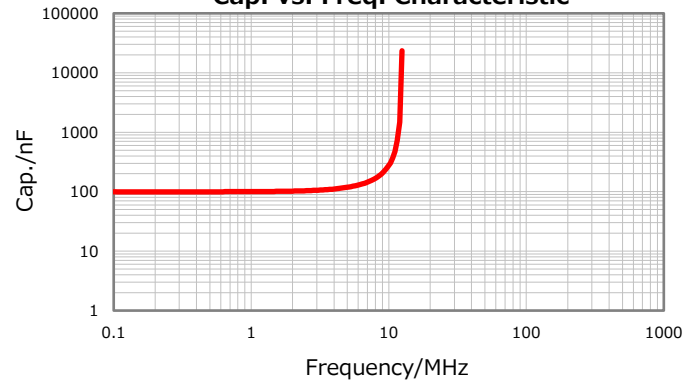
## Temperature Characteristics



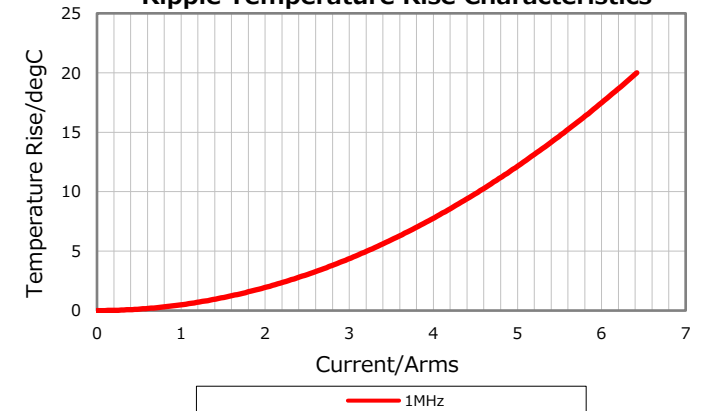
## |Z|, ESR vs. Freq. Characteristics



## Cap. vs. Freq. Characteristic



## Ripple Temperature Rise Characteristics



Characterization Sheet ( Multilayer Ceramic Chip Capacitors )



All specifications are subject to change without notice.