

CLASS No.	TITLE	
	SLIM TYPE SLIDE POTENTIOMETER	

ELECTRICAL

- Overall resistance : 10kΩ±20%
- Minimum resistance : Across term.1-2 30Ω max. Across term.2-3 50Ω max.
- Taper : B Taper
- Rated power : 0.025Watts
- Rated voltage : Rated voltage = $\sqrt{P \cdot R}$ (V)
P : rated power (W)
R : nominal overall resistance (Ω)
When the rated voltage exceeds the maximum operating voltage the maximum operating voltage shall be the rated voltage.
Maximum operating voltage : A.C.100 V D.C.10 V
- Sliding life test : 10,000 cycles.

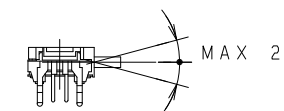
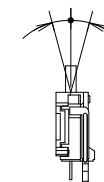
*Lever shall be operable with speed of 20 mm per sec. without noise by static electricity.

					ALPSALPINE CO.,LTD.				
					APPD.	CHKD.	DSGD.	TITLE	
					'96-04-01	'96-04-01	'96-04-01	SPECIFICATIONS	
					Y.YOSHIOKA	H. ITO	H. KIMURA	DOCUMENT NO.	
								RS15H11A0B14E (1/3)	
SYMB	DATE	APPD	CHKD	DSGD					

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MECHANICAL

- Travel : Specified in particular Figure.
- Operating force : 0.6 $\frac{1}{2}$ N (Note 1)
(Note 1) Measuring temperature : 5°C - 35°C
Measuring point : 2mm from the top of the lever.
Sliding speed : 20 mm per sec.
- Stop strength :
30N at a position 2mm from the base of the lever.
- Lever lateral play :
When 2.5N is applied in a sidewise direction to a point 2mm from the base of the lever, the bothside movement of the lever, shall be less than 1.6mm.
- Lever strength :
To be resistant with 30N static force of pull or push applied to lever in thrust direction for 10 seconds without damage.
- Resistance to soldering heat : 3 sec. max. at 350°C
- Lever inclination : Measure a sample that is inserted firmly in a P.C.B



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								RS15H11A0B14E (2/3)	
SYMB	DATE	APPD	CHKD	DSGD					

ご使用上の注意

PRECAUTION IN USE

1. 偏心ツマミをご使用になる場合

レハ[®]の中心より離れたところを作用点としてご使用になる場合、可能な限り
下図A寸法を短くしてご使用下さい。

If it will be used the operating point away from the center line of the lever, it should be shorter as possible.

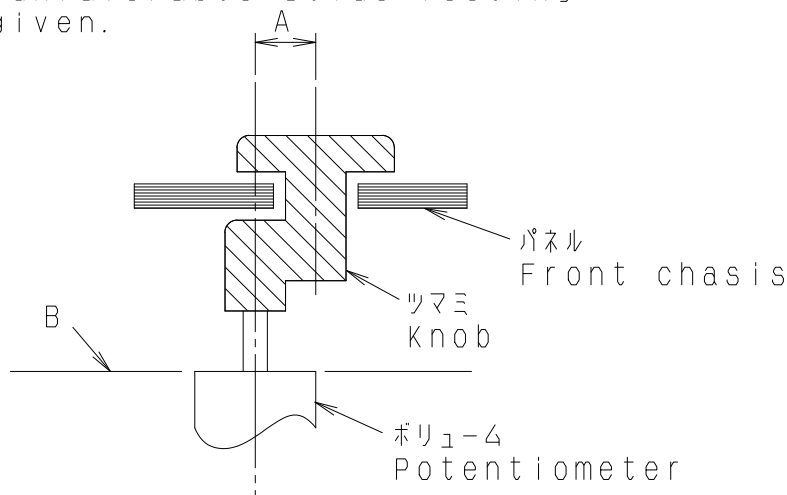
2. レハ[®]の長さについて

レハ[®]の長さについては、ツマミを含めて、下図B面より極力短いものをご使用願います。レハ[®]の長さについては、作用点までの距離が短いほど
しゅう動感が良好となり、長いほど好ましくない感触になります。

About the length of lever

If conditions permit, it is advisable to use the shortest possible lever.

The longer the length up to operating point, the more unfavorable slide feeling will be given.



3. レハ[®]の駆動に関しては上記内容を考慮の上、セット実装を行い

あらかじめ異常のないことをご確認願います。

Regarding the operation of the lever, please consider the above mentioned, and make sure nothing is wrong with the operation under installing in your appliance that you plan to use our products actually.

4. ツマミ挿入及びレハ[®]操作は、ホ[®]リウムマウント基板に

ソリ(曲がり)のない状態で行って下さい。

Knob assembly on the lever and functioning the lever to be performed under the condition of P.C.B. without warp.

5. 電圧調整回路において出力側のインピーダンスが低い場合には抵抗体と摺動子間の接触抵抗の影響を受けることがありますのでインピーダンスを公称全抵抗値の100倍以上に設定願います。

There is a possibility that might be affected by contact resistance of resistive element and wiper in case of low impedance of output side in voltage regulation circuit. for this reason, we require that you adjust to impedance of output side more than 100 times of total resistance.

					ALPSALPINE CO.,LTD.			
					APPD.	CHKD.	DSGD.	TITLE
					通・設計試作 '07-4-5 池之上	通・設計試作 '07-4-5 大矢	通・設計試作 '07-4-5 玉田	スライト [®] ホ [®] リウム仕様書 SPECIFICATIONS
ORIGINAL	1991-07-03	Y・Y	K・N	S・A				DOCUMENT NO.
SYMB	DATE	APPD	CHKD	DSGD				450001-200

RS15H11A0B14E

はんだ付け条件

FOLLOW THE NEXT CONDITIONS FOR SOLDERING

1. はんだ SOLDER

JIS Z 3282に規定のA30C5はんだを使用

A30C5 solder specified in JIS Z 3282.

2. 使用基板 BOARD IN USE

両面スルーホール基板又は、片面銅張積層板 板厚 $t=1.6\text{mm}$

Double-faces through-hole board or Single-face copper laid laminate board.

Plate thickness (t)= 1.6mm

3. 自動はんだ<DIP条件>

(1)レハ"ー位置 センター付近に設定願います。

(2)フラックス比重 0.83 ± 0.01 (発泡式)

(3)フラックス高さ フ"リント基板の板厚の半分の位置にフラックスの上面が接するレハ"ル(図1)
又、ホ"リウム挿入面への流れ込みのないこと。(フラックス上がり、飛散に注意)

(4)フ"リヒート温度 100°C max. 時間1分以内。(フ"リント基板のホ"リウム挿入側の温度)

(5)はんだ温度 260°C max. 時間5秒以内. はんだ回数は1回までとする。

IN THE CASE OF DIP SOLDERING

(1)State of potentiometer

Position a lever in the vicinity of center.

(2)Specific Gravity of Flux

0.83 ± 0.01 (foaming type)

(3)Height of Flux face

A level of the upper face of flux for reaching the position at a half of the plate thickness of printed board. (Fig. 1)

Further, no flow of flux invading on the surface of printed board on the side of installing potentiometer is allowed.

(4)Preheat condition

100°C max., within 1 minute

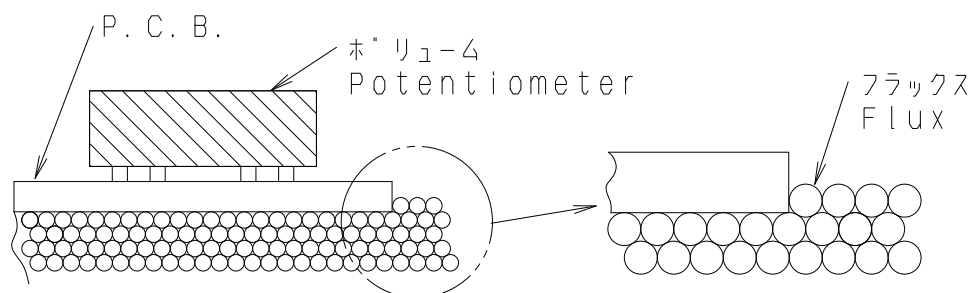
(Temperature on the side of installing printed board is designated.)

(5)Soldering condition

Solder temperature; 260°C max.

Soldering period ; within 5 seconds

Time of soldering ; only one time is permitted



(Fig. 1)

4. 手はんだ IN THE CASE OF MANUAL SOLDERING

はんだ温度 350°C max. 時間3秒以内 はんだ回数は1回までとする。

Solder temperature ; 350°C max.

Soldering period ; within 3 seconds

Time of soldering ; only one time is permitted

					ALPSALPINE CO., LTD.			
					APPD.	CHKD.	DSGD.	TITLE
					PD1-ENG1 '95.7.24 YOSIOKA	PD1-ENG1 '95.7.24 KIMURA	PD1-ENG1 '95.7.24 Y. SAITOH	スライト" ホ"リウム 仕様書 SPECIFICATIONS 1/2
ORIGINAL	'91-9-3	Y・Y	S・A	S・S	DOCUMENT NO.			
SYMB	DATE	APPD	CHKD	DSGD	450001-201			

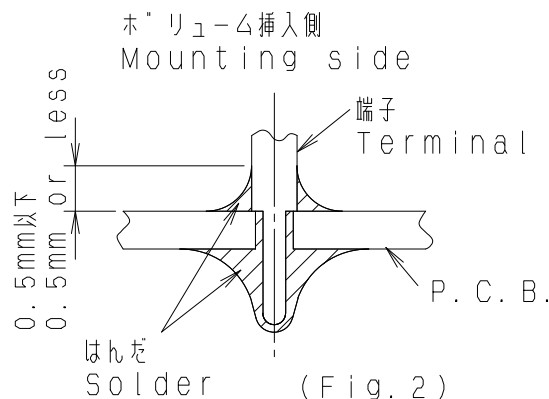
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5. 注意事項

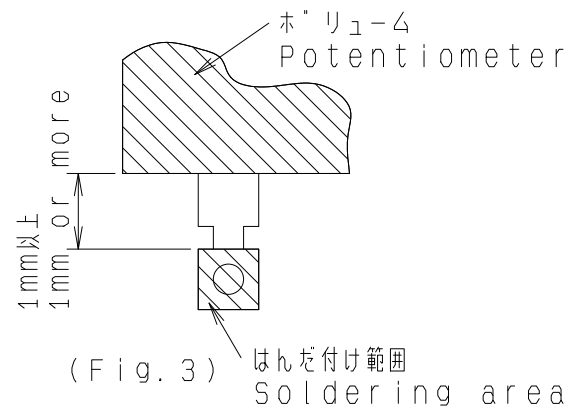
- (1) はんだ付けの際に、端子にストレスを加えないで下さい。例えば、端子に熱を加えたまま製品を動かしますと、かしめ力[※]及び電気的特性が劣化する恐れがあります。
- (2) 両面スルーホール基板を使用する場合は、ホ[※]リウム挿入側の端子取付穴に、はんだランド[※]がないようにご配慮願います。ホ[※]リウム挿入側での配線が必要な場合は端子取付穴からの直接取り出しを避けスルーホール配線用の穴を設けるなどのご配慮をお願いします。
- (3) ホ[※]リウム挿入側へのはんだ上がりは、はんだ熱による端子接触不良の発生原因となりますので(図2)を参照願います。
- (4) リート[※]配線の場合、ホ[※]リウム本体と、はんだ付け部の距離を1mm以上開けてはんだ付け願います。(図3)
- (5) はんだ付けによるホ[※]リウムへの影響は、フ[※]リント基板の大きさ、ホ[※]リウムの取付け位置、はんだ槽の大きさ、等により異なりますのであらかじめ実使用状態で実施し、異常のないことを確認の上、はんだ付けして下さい。

MATTERS TO BE NOTED

- (1) Do not add any stress on terminals in the case of soldering. For instance, forced movement of potentiometer with terminals being heated may probably deteriorate the electric features due to generation of looseness in connection between resistant board and terminals.
- (2) Avoid use of double-faces through-hole board as much as possible. If it is necessary to use it, Do not apply through-hole plating to a hole in which a potentiometer is inserted, and install a land to which terminals are soldered only on a face oppsite to the face on the side of installing potentiometer.
- (3) Use caution to soldering process so as to prevent solder from rising up to the surface of printed board on the side of installing potentiometer, because defective contact may take place in terminal connecting part due to soldering heat . (Fig.2)
- (4) In the case of lead wiring, solder it so that a gap of 1 mm or more may be reserved between the potentiometer body and soldering part. (Fig.3)
- (5) The grade of influence of soldering exerted on the potentiometer depends upon the size of a printed board, installing position of the potentiometer, and the size of a solder bath etc. Therefore, make sure, in advance, of no abnomal state under the conditions of soldering to be carried our at present.



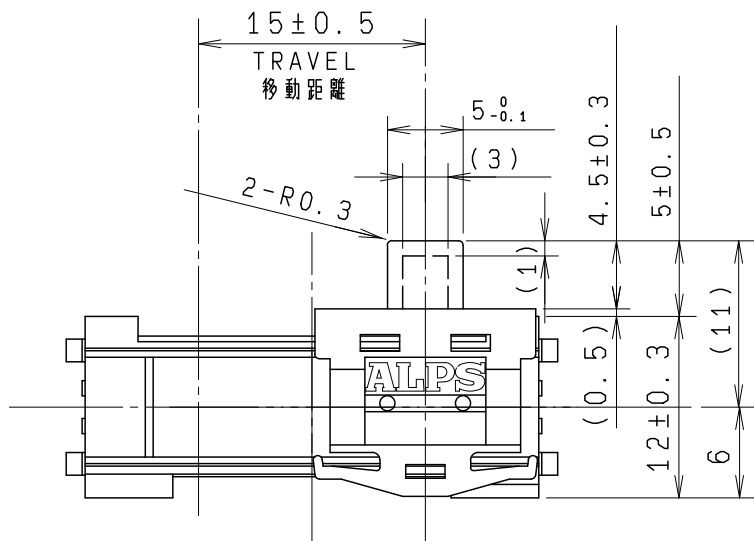
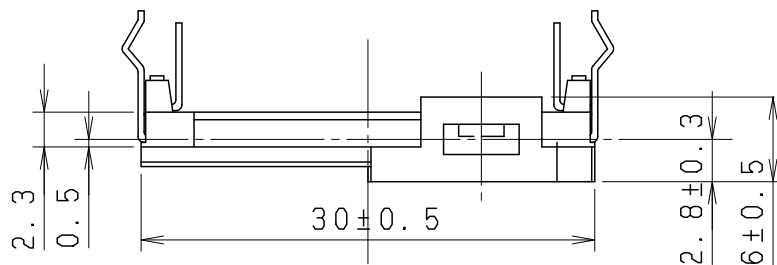
(Fig. 2)



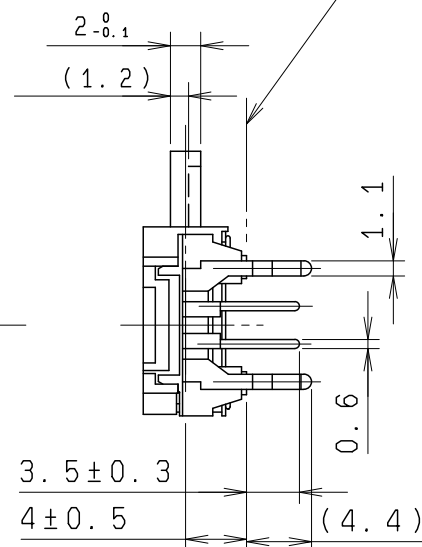
(Fig. 3)

					ALPSALPINE CO.,LTD.			
					APPD.	CHKD.	DSGD.	TITLE
					PD1-ENG1 '95. 7. 24 YOSIOKA	PD1-ENG1 '95. 7. 24 KIMURA	PD1-ENG1 '95. 7. 24 Y.SAITOH	スライト [※] ホ [※] リウム 仕様書 SPECIFICATIONS 2/2
ORIGINAL	'91-9-3	Y・Y	S・A	S・S	DOCUMENT NO.			
SYMB	DATE	APPD	CHKD	DSGD	450001-201			

RS15H11A0B14E

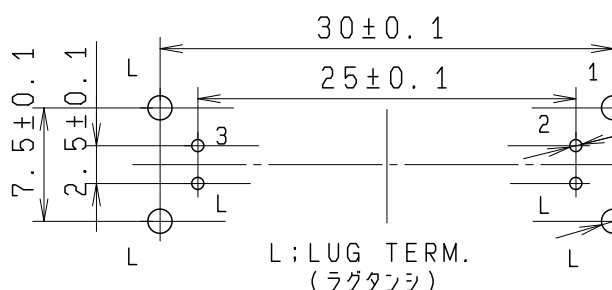


MOUNTING SURFACE
(取付面)



MOUNTING HOLE DETAIL (取付穴寸法図)

VIEWED FROM MOUNTING SIDE (挿入側より)



LEVER SIDE

4-φ0.8^{+0.1} HOLES
4-φ1.6^{+0.1} HOLES

L; LUG TERM.
(ラグタンジ)

Note 1. Top side of knob shall be mounted to lever within 20mm length.
ツマミ長さは、レバー長さを含めて20mm以内でご使用願います。

回路図

CIRCUIT



指定なき部分の許容差
TOLERANCES UNLESS OTHERWISE SPEC

$L \leq 10$	± 0.3
$10 < L < 100$	± 0.5
$100 \leq L$	± 0.8
角度 ANGULAR DIMENSION	$\pm 5^\circ$

PART NO.		NAME		MATERIAL NAME / CODE			FINISH			
					ALPSALPINE CO.,LTD.					
					DSGD. セツケイ Y, WATANABE ' 92-06-08		SCALE 2 : 1		NO. S152KNA11 RS15H11A0A05A	
					CHKD. K, SATOU ' 92-06-08				TITLE SLIDE POTENTIOMETER SINGLE UNIT	
					APPD. Y, YOSHIOKA ' 92-06-08		UNIT mm		DOCUMENT NO. S2K-A212	
SYMB	DATE	APPD	CHKD	DSGD						